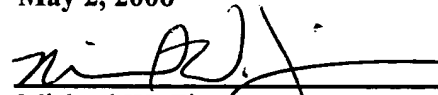




2FW

PATENT
Docket No. Intel/17225

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Tian et al.)	I hereby certify that this paper is
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Assignee: Intel Corporation)	Box 1450, Alexandria, VA 22313-
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For: "Methods And Apparatus For)	
Reducing Memory Latency In A Software)	May 2, 2006
Application")	
)	
Group Art Unit: 2121)	Michael W. Zimmerman
)	Registration No.: 57,993
Examiner: Unknown)	Agent for Applicant(s)

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Submitted herewith is a copy of a Written Opinion of the International Searching
Authority for the International application no. PCT/US2004/032212.

TIME OF FILING

This information disclosure statement is being filed, to the best of the undersigned's knowledge, before the mailing date of a first Office action on the merits. In accordance with 37 CFR §1.97(b), no certification or fee is required.

METHOD OF PAYMENT

No fee is required.

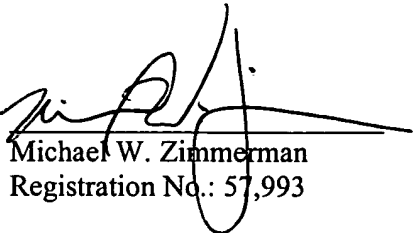
The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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Respectfully submitted,

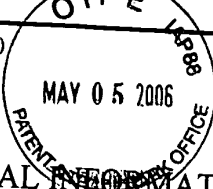
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By:


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May 2, 2006

Agent for Intel Corporation

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>			Atty. Docket No.	Serial No.
			Intel/17225	10/677,414
			Applicant	
			Tian et al.	
			Filing Date	Group Art Unit
			October 2, 2003	2121

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)	
C01	Submitted herewith is a copy of a Written Opinion of the International Searching Authority for the International application no. PCT/US2004/032212.
C02	

EXAMINER	DATE CONSIDERED
<p>*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.</p>	